

DEVELOPMENT OF A DC-DC CONVERTER FOR THE MU3E EXPERIMENT

SOPHIE GAGNEUR
MU3E COLLABORATION
DPG FRÜHJHRSTAGUNG
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3/15/2021

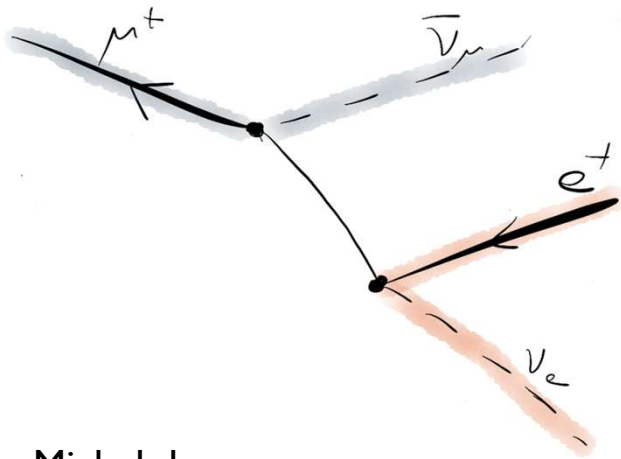


3/15/2021



THE MUZE EXPERIMENT

MOTIVATION

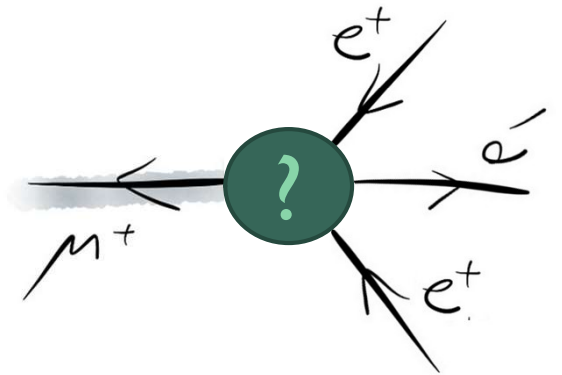


Michel decay

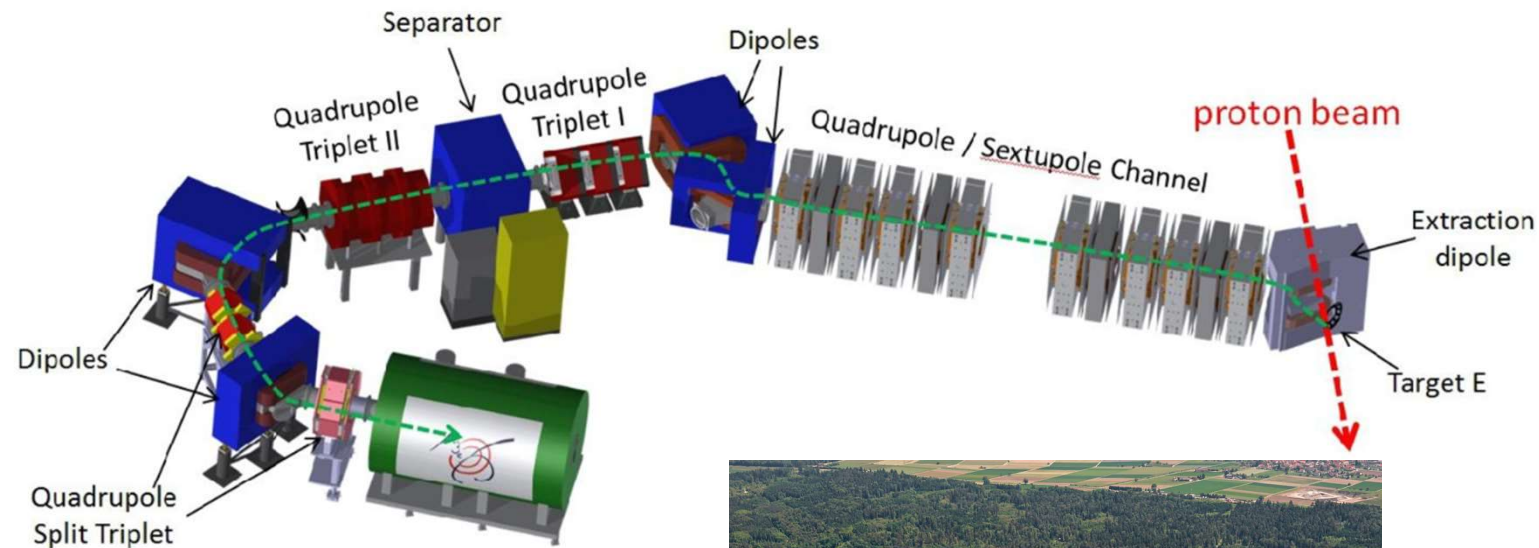
- Branching ratio nearly $\sim 100\%$
- $\mu \rightarrow eee$ suppressed in the standard model
→ lepton flavour violation



Theories of physics
beyond the
standard model



THE EXPERIMENT



Phase I

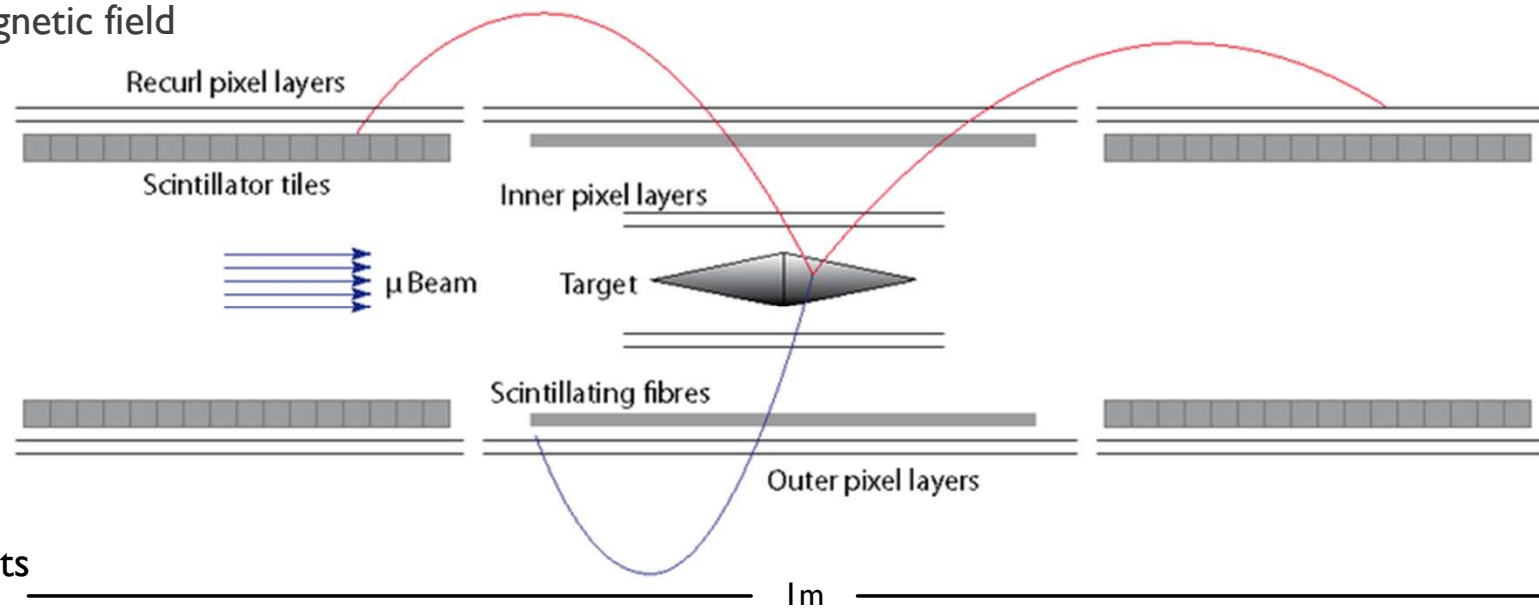
- Muon beam: $10^7 - 10^8$ muons/seconds
- Sensitivity goal: $B(\mu \rightarrow eee) \leq 2 \cdot 10^{-15}$



Paul Scherrer Institute,
Villigen, Switzerland

THE DETECTOR

- Placed inside a IT strong magnetic field
- Three detector subsystems



- Pixel detector (MuPix sensors)
 - Accurate track and vertex reconstruction

- Scintillating fibres
 - Scintillating tiles
- } Exact timing measurements

POWER REQUIREMENTS

- Relatively low voltages required by the detector components (1-3.3V)
- Cables are very long → high losses through the cables
- Thicker cables are not possible according to size
- Solution: DC-DC converters close to the detector parts step a 20V input power down to the required value
- Power distribution is segmented into power partitions:



- One converter per power partition
- 126 in total → 10kW in total

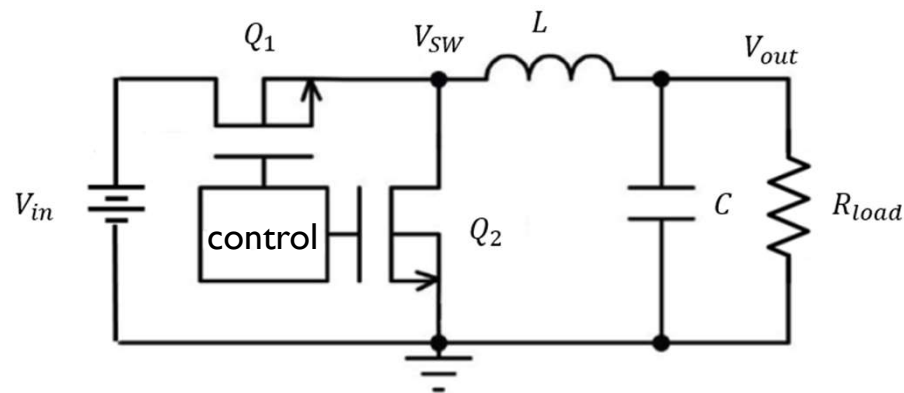
Further talk about the mu3e power distribution:
Wed. 16:00 T66.1 Lucas Sebastian Binn



THE MU3E DC-DC CONVERTER

LAB MEASUREMENTS & TEST BEAM RESULTS

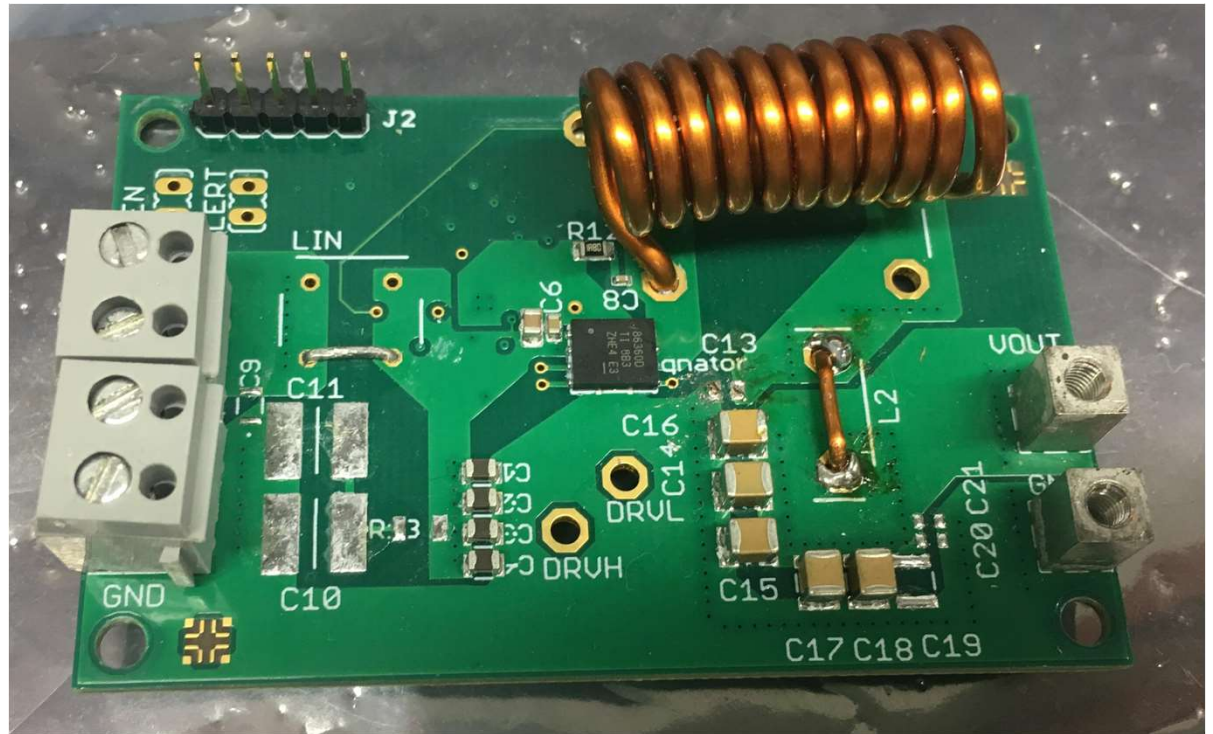
A SYNCHRONOUS BUCK CONVERTER



- Not regulated DC input voltage is converted into a regulated DC output voltage
- Regulation is derived from high-frequency switching of two MOSFETs
 - Producing a rectangular voltage
- Output signal is smoothed by a coil and capacitors
- Switching behaviour is synchronised to prevent short circuits → **synchronous buck converter**

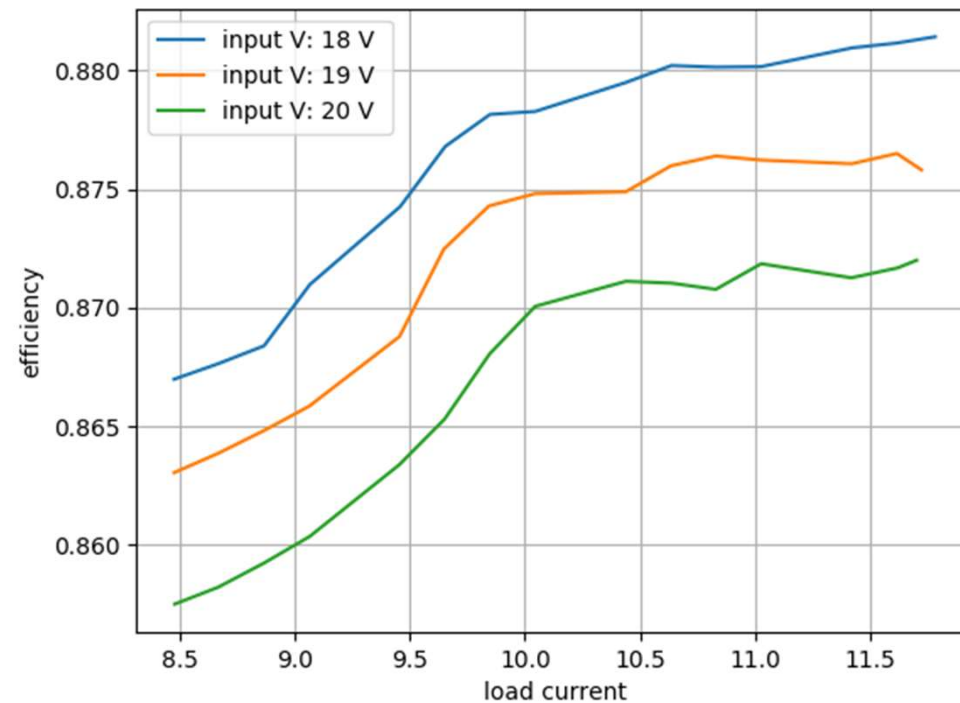
DC-DC CONVERTER FOR MUPIX & MUTRIG

- $V_{in} = 20V$
- $V_{out} = 2.1V$
- $L = 0.55\mu H$
 - Air coil
- $C = 22\mu F$
- $f_{switch} = 1MHz$
- Efficiency: 87.0% at 20A



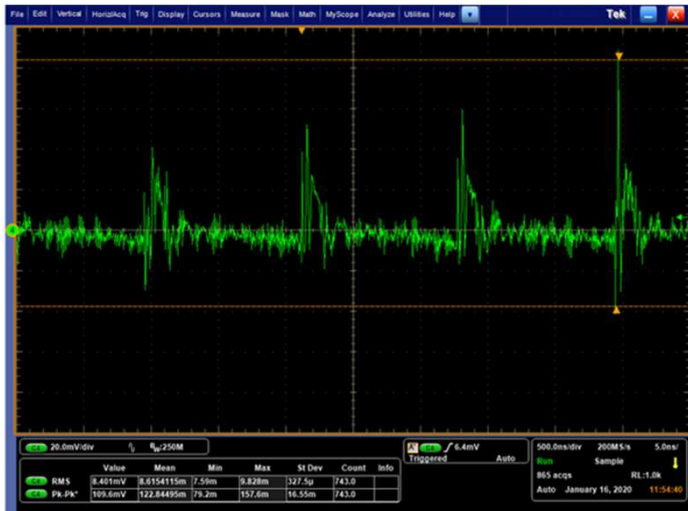
DC-DC CONVERTER FOR MUIPIX & MUTRIG

- $V_{in} = 20V$
- $V_{out} = 2.1V$
- $L = 0.55\mu H$
 - Air coil
- $C = 22\mu F$
- $f_{switch} = 1MHz$
- Efficiency: 87.0% at 10A

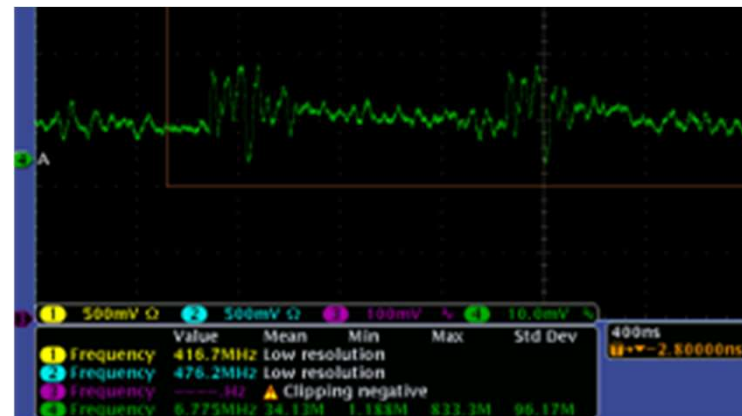
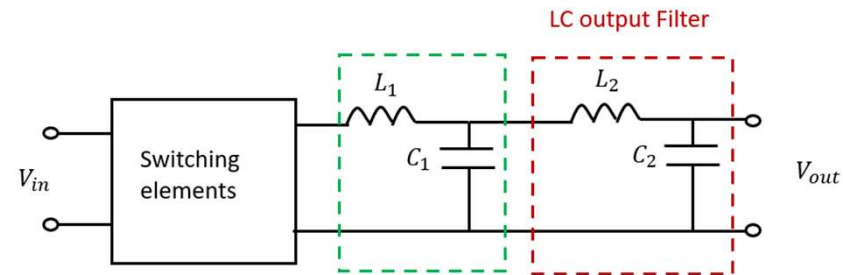


OUTPUT SIGNAL & FILTERING

$V_{in} = 20V$
 $V_{out} = 2.1V$
 $I_{out} = 5A$
 $f_{sw} = 1MHz$

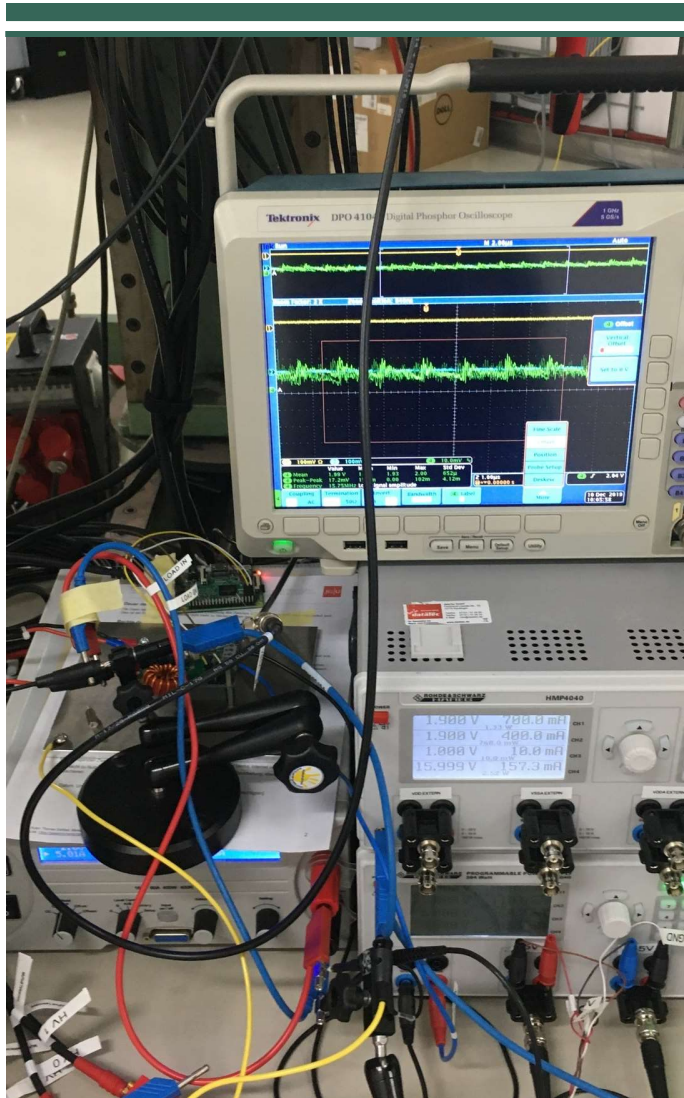


➔ Output filter required



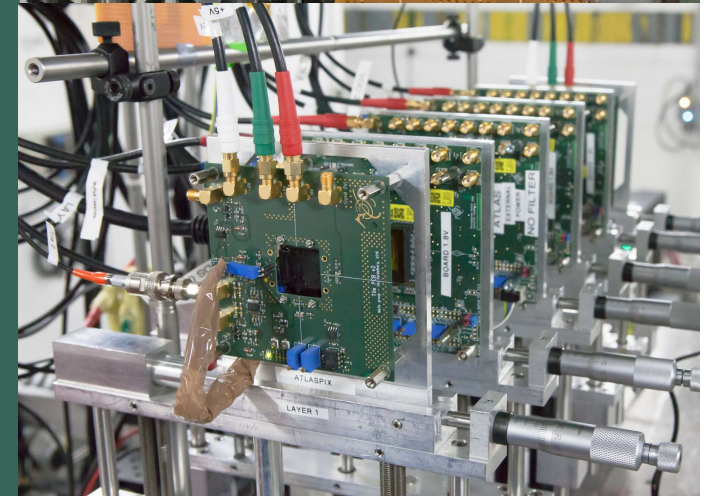
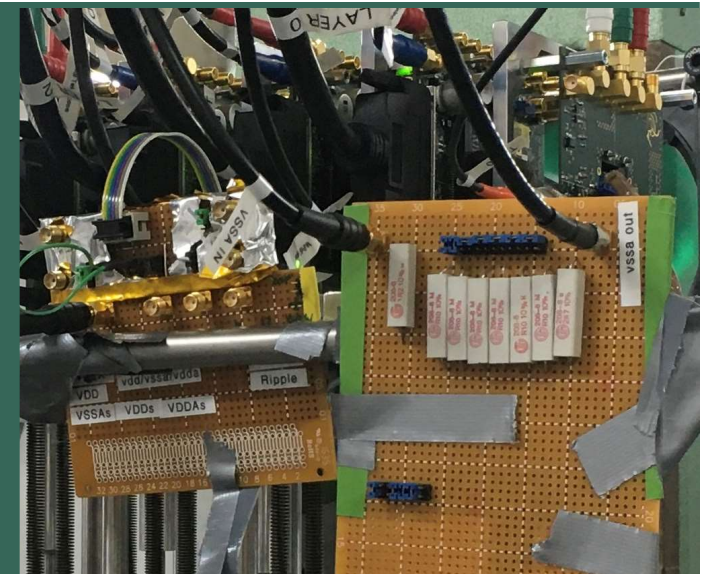
- Ripple height: ~30mV
- Goal: ≤10mV

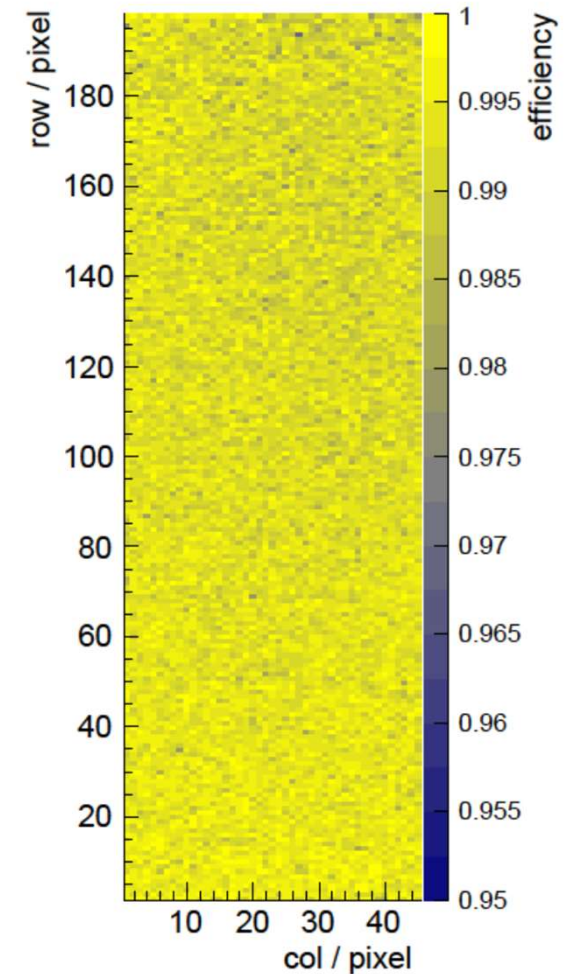
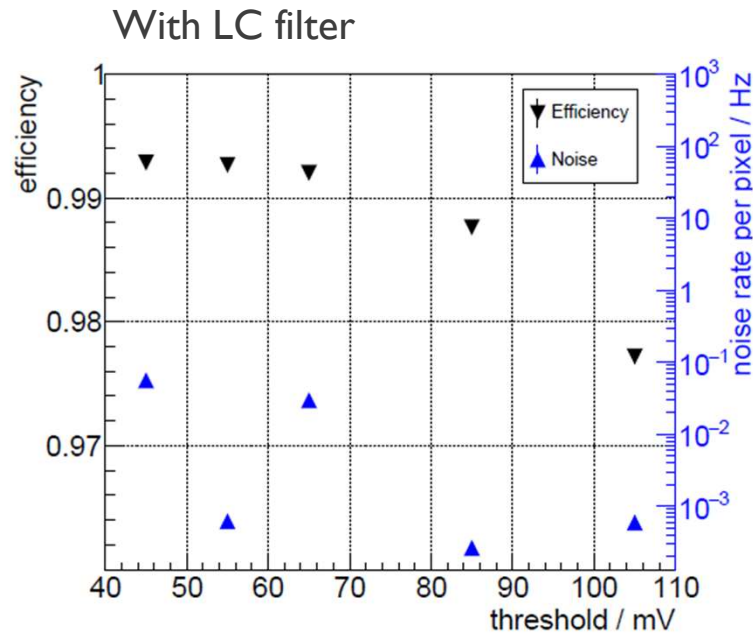
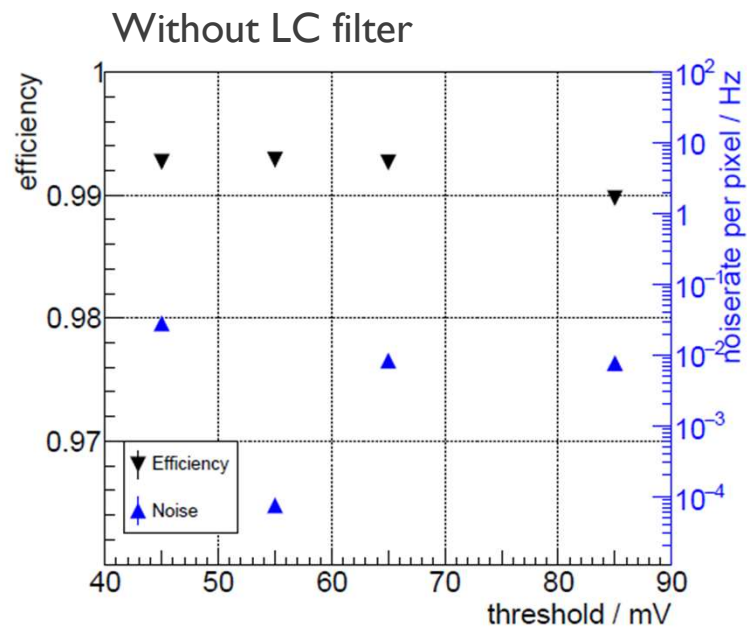
- Ripple height: ~15mV
➔ reduction of 50%



TEST BEAM SET UP

- Powering of a MuPix sensor
- Sensor placed in a MuPix telescope
- Converter supplied with 20V
- Controlled by a Raspberry Pi
- Determine efficiency and noise level
 - Threshold scans
- Signal in a pixel is just registered if its level is above a certain threshold
- Scans with and without a second filter





TEST BEAM RESULTS

- Efficiency: 99.3%
- Noise always below 1 Hz
- No patterns in the efficiency map
- No difference between the two converters configurations

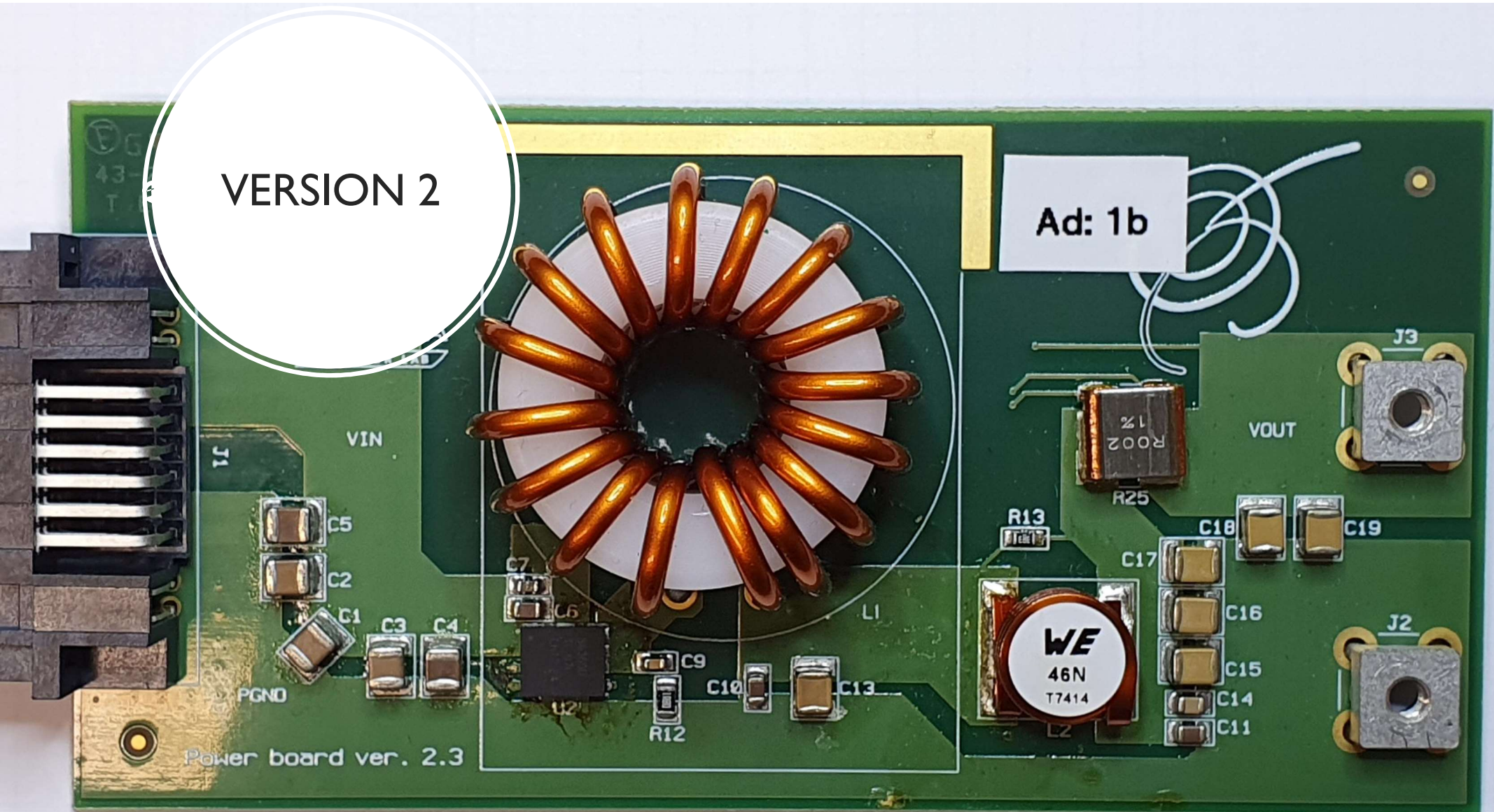


THE SECOND VERSION

IMPROVEMENTS & NEW FEATURES

VERSION 2

Ad: 1b



Power board ver. 2.3

WE
46N
T7414

R25
2002

VIN

VOUT

J1

J3

PGND

L1

J2

C5

C2

C1

C3

C4

C7

C6

C9

C10

C13

R12

R13

C17

C18

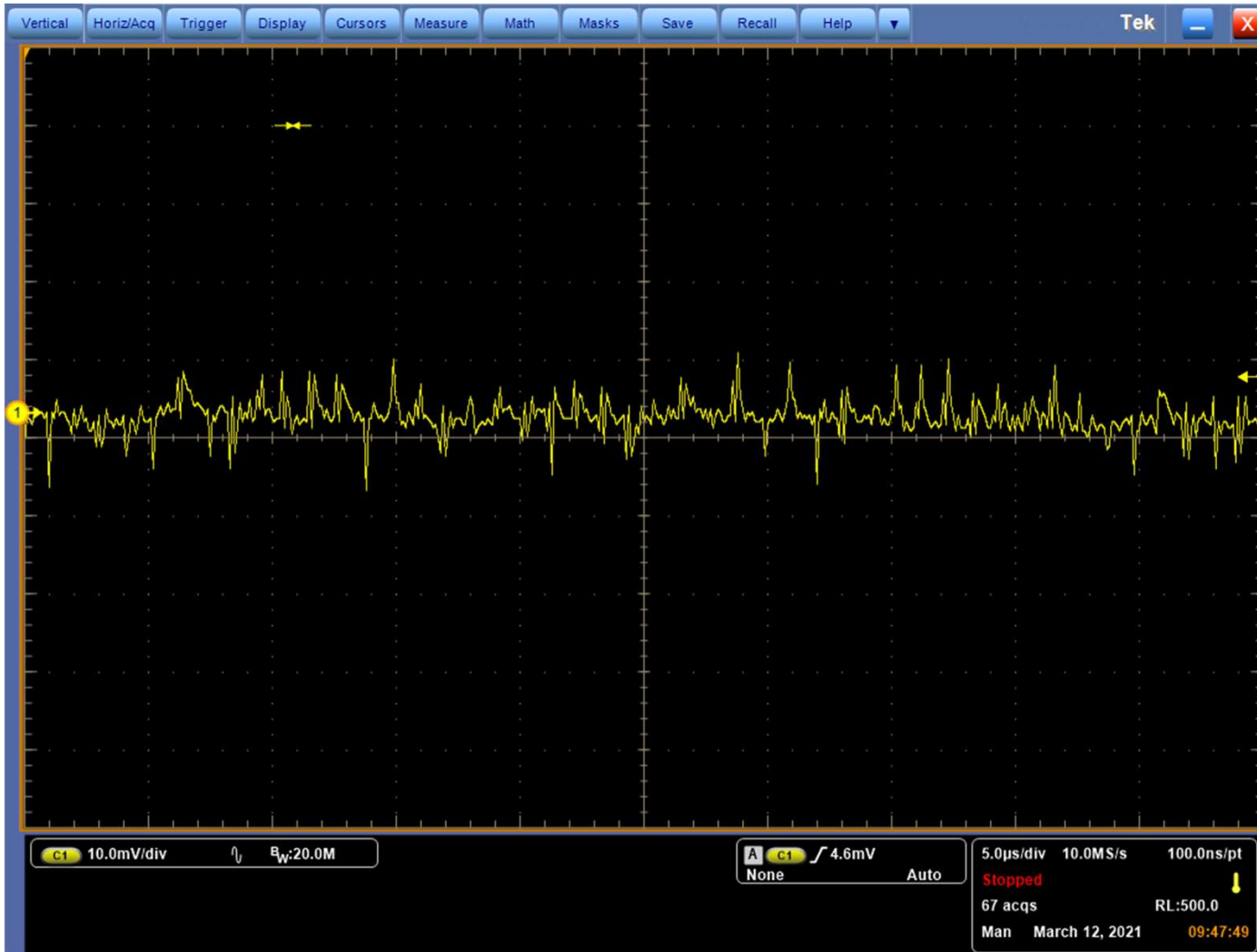
C16

C15

C14

C11

C19



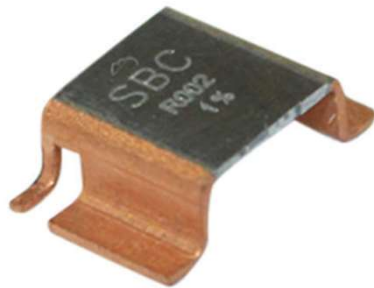
OUTPUT
SIGNAL

CURRENT SENSE MEASUREMENT

- Monitoring the current of the converter

➡ voltage drop across a shunt resistor is measured

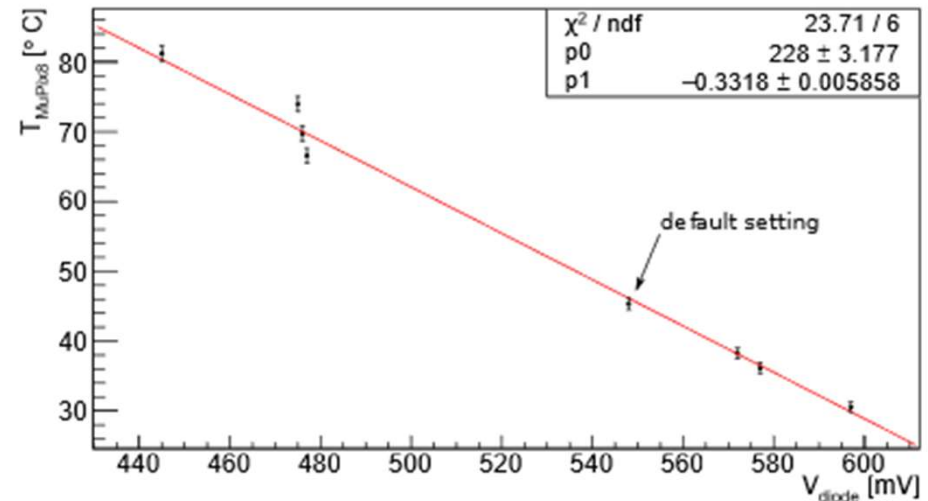
➡ the voltage drop can be converted in the corresponding current



- Instrumentation amplifier: INA326, Texas Instruments
- Voltage drop of 50mV selected
- At 20A: 2.5m Ω shunt resistor \rightarrow 2W power dissipation
- Read out by ADCs
- Tested and working stable

TEMPERATURE INTERLOCK SYSTEM

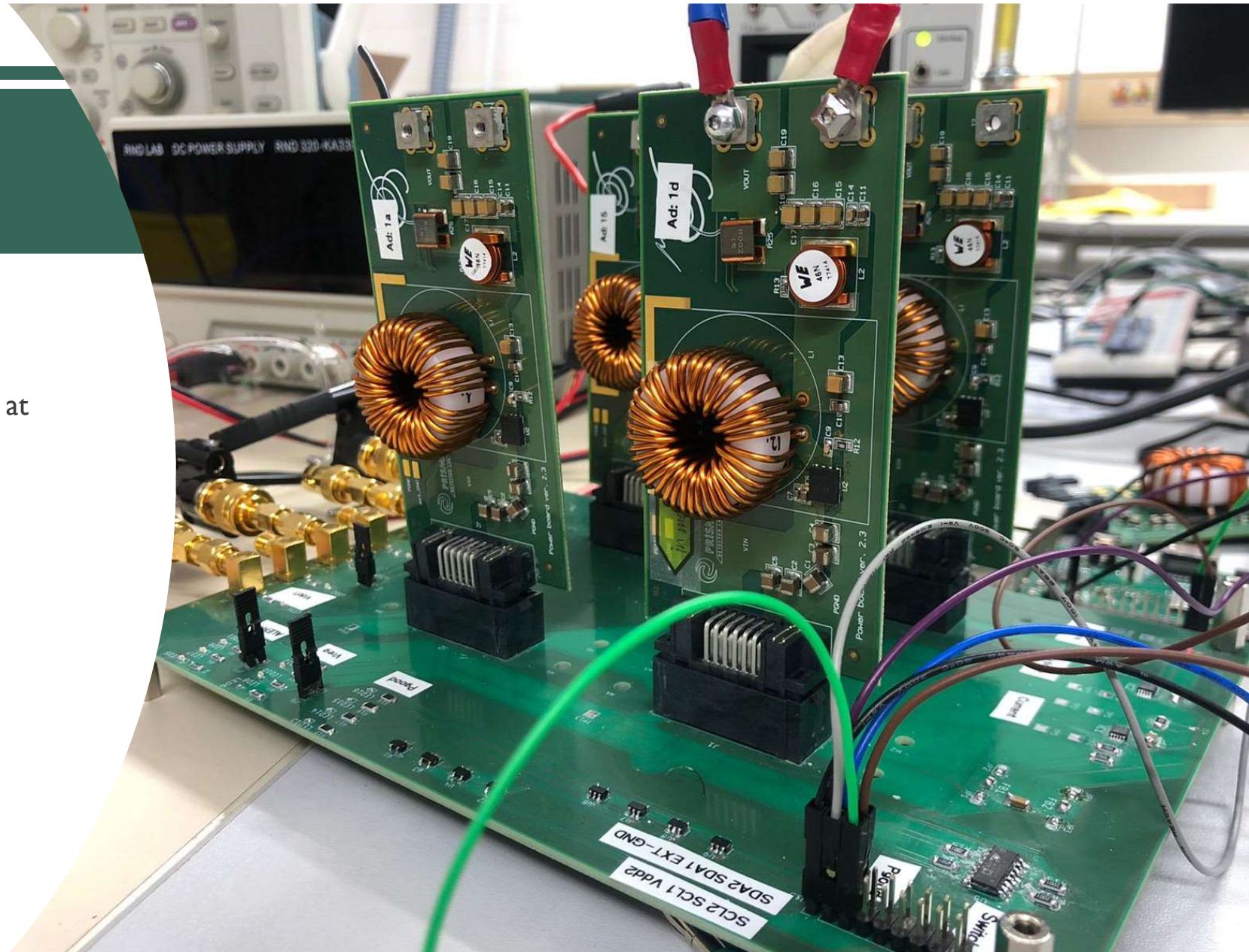
- High power means strong heat development
- Complete detector must be cooled
- System to make sure the detector is running just when the cooling system is on
- Therefore the MuPix temperature diode is used
- Temperature rises \Rightarrow converters are switched off



$$T_{\text{MuPix}} = 228.0^{\circ}\text{C} - 0.332^{\circ}\frac{\text{C}}{\text{mV}} \cdot V_{\text{bias,diode}}$$

BACKPLANE

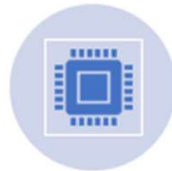
- Controlling of 4 converters at the same time
- Later 16 boards
- Monitoring of all relevant voltages via ADCs



CONCLUSION & OUTLOOK



The new features of the second version of the mu3e DCDC converter are working



Test beam with several MuPix sensors powered by the converters



Behaviour at full load (20A)



Cooling system needs to be tested (already designed)

THANK YOU
FOR
YOUR
ATTENTION

QUESTIONS?

ND
ND

PRISMA
DETECTOR LAB

VIN

C5
C2
C1

C3
C4

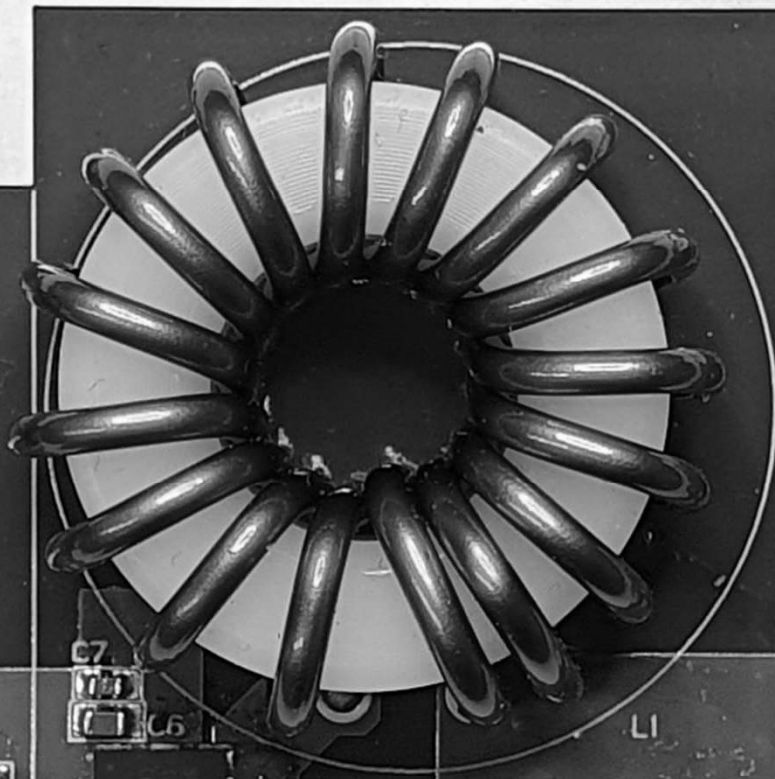
C7
C6

C9
R12

C10

C13

ard ver. 2.3



Ad: 1b

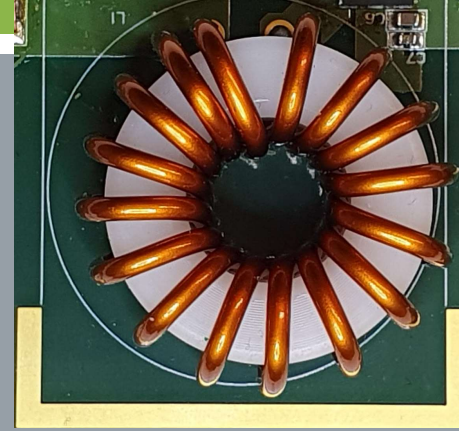
R002
1%
R25

R13

WE
46N
T7414
L2

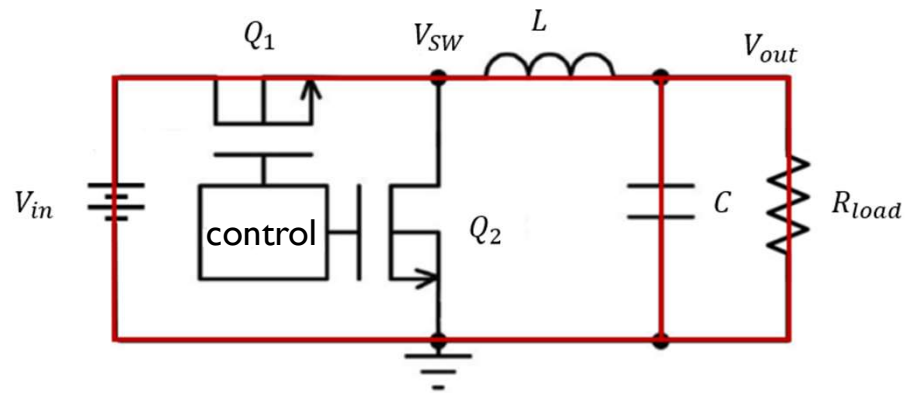
C18
C17
C1
C1
C14
C11

THANK YOU FOR YOUR ATTENTION

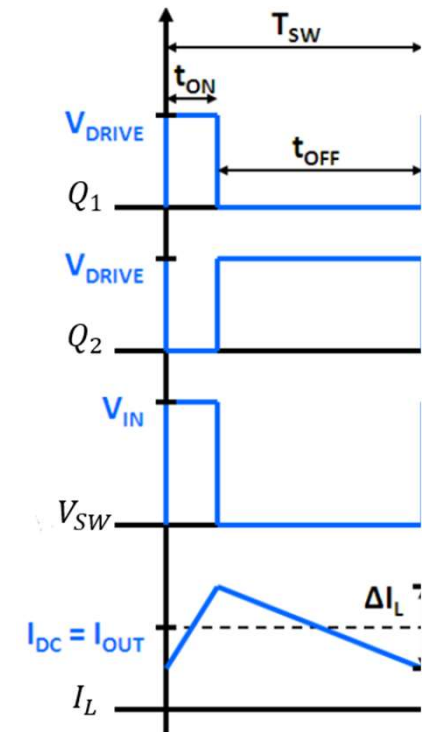


WORKING PRINCIPLE

Phase I: Q_1 is on and Q_2 is off for a time t_{on}

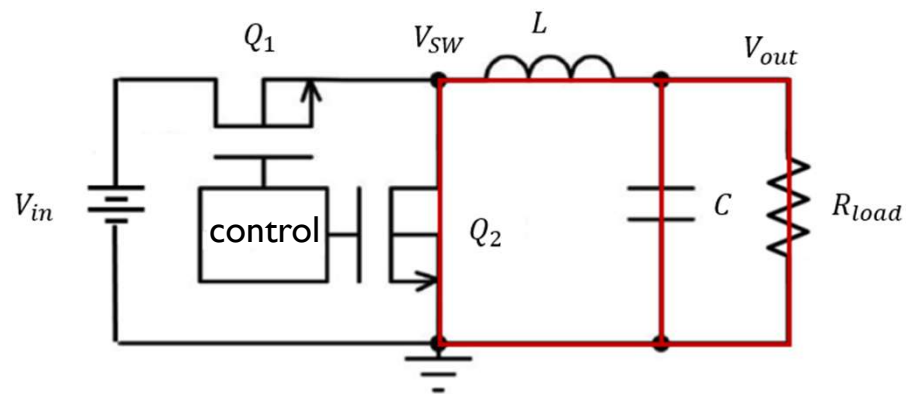


- Current through the coil increases linearly
- The coil provides the current for the load
- The excess current is stored in the capacitor

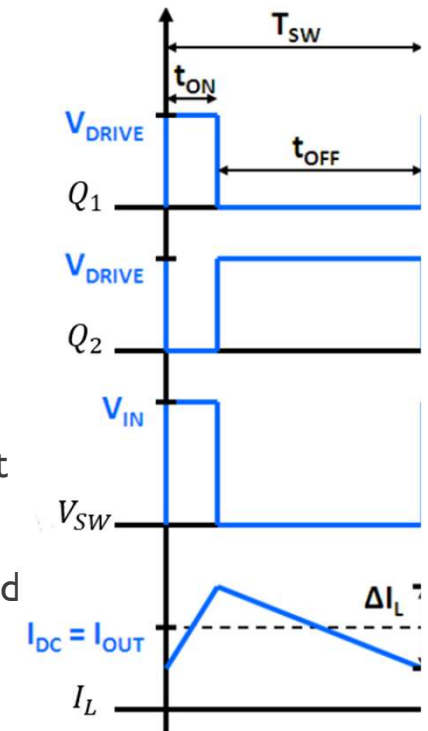


WORKING PRINCIPLE

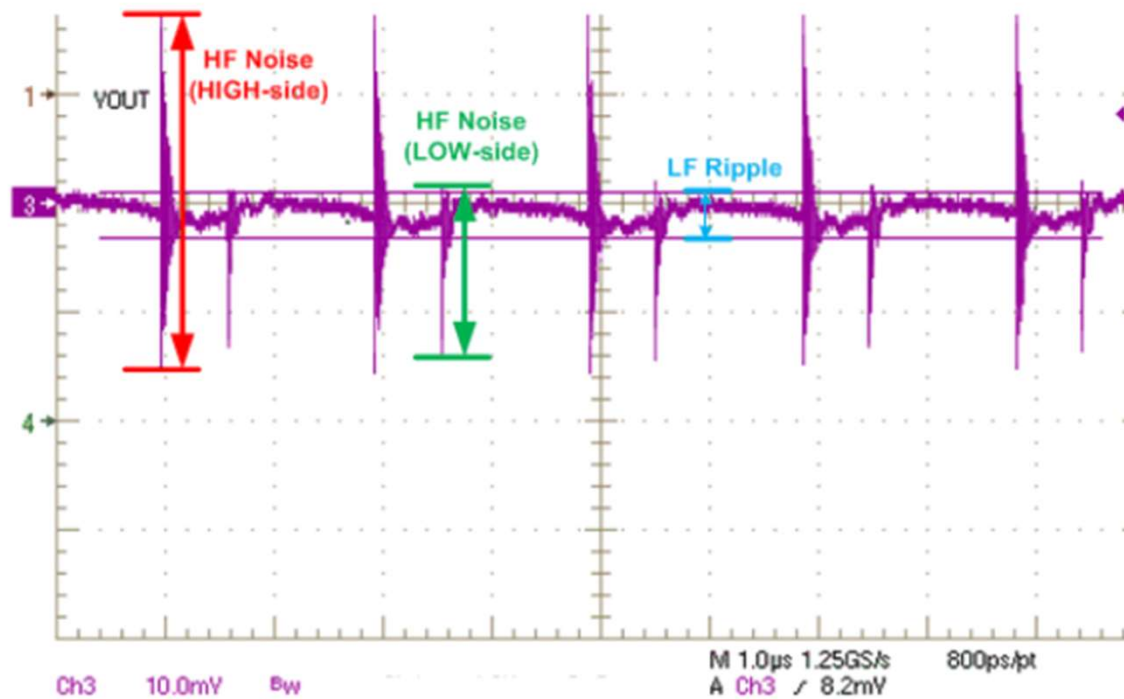
Phase II: Q_1 is off and Q_2 is on for a time t_{off}



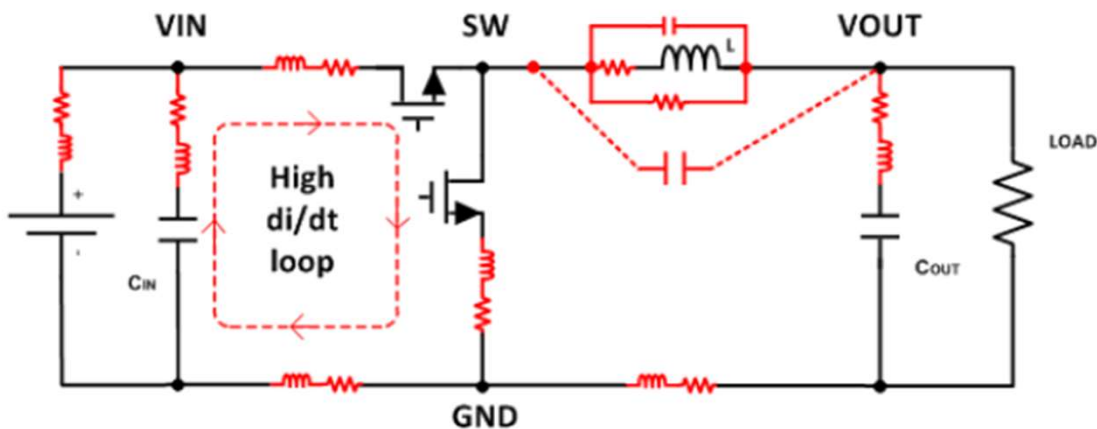
- Voltage source is disconnected
- Current through the coil decreases linearly
- Current from the coil is not sufficient
- Current deficit compensated by the capacitor



NOISE IN A SWITCHING BUCK CONVERTER



HIGH FREQUENCY NOISE



- Main cause for HF noise: current loops
- The changing magnetic field created by the loop produces an inductive voltage:

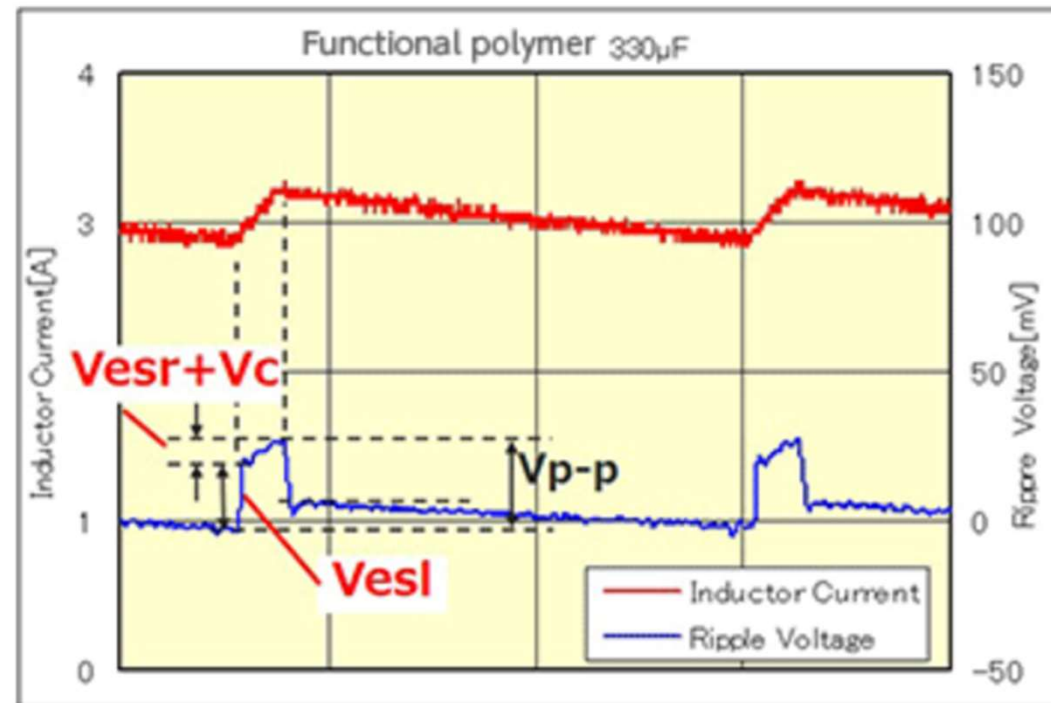
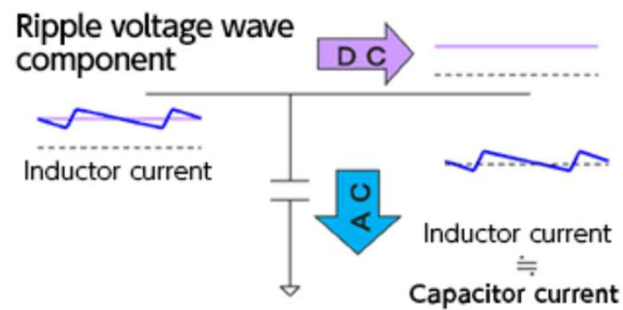
$$V_L = -\frac{d\phi}{dt} = -L \frac{di}{dt},$$

$$\phi = \int_A B \cdot dA = L \cdot I$$

- Strongly depends on loop geometry
- High di/dt loop must be kept as small as possible
- Noise from the input can couple to the output

LOW FREQUENCY NOISE

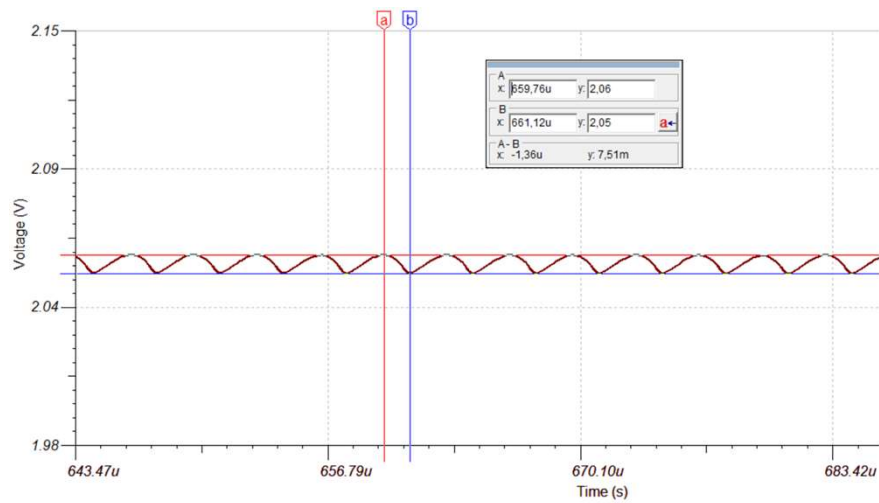
- Combination of inductor ripple current and output capacitor impedance
- Different parasitic components have different influence on the output signal



TEST BEAM MEASUREMENTS

- To determine efficiency and noise level threshold scans were performed
 - Signal in a pixel is just registered if its level is above a certain threshold
 - The lower the threshold the higher the efficiency
 - But higher noise level too
- One scan performed with a second LC filter and one scan without
- Short measurement time → low statistics

RE-DESIGNED OUTPUT FILTER



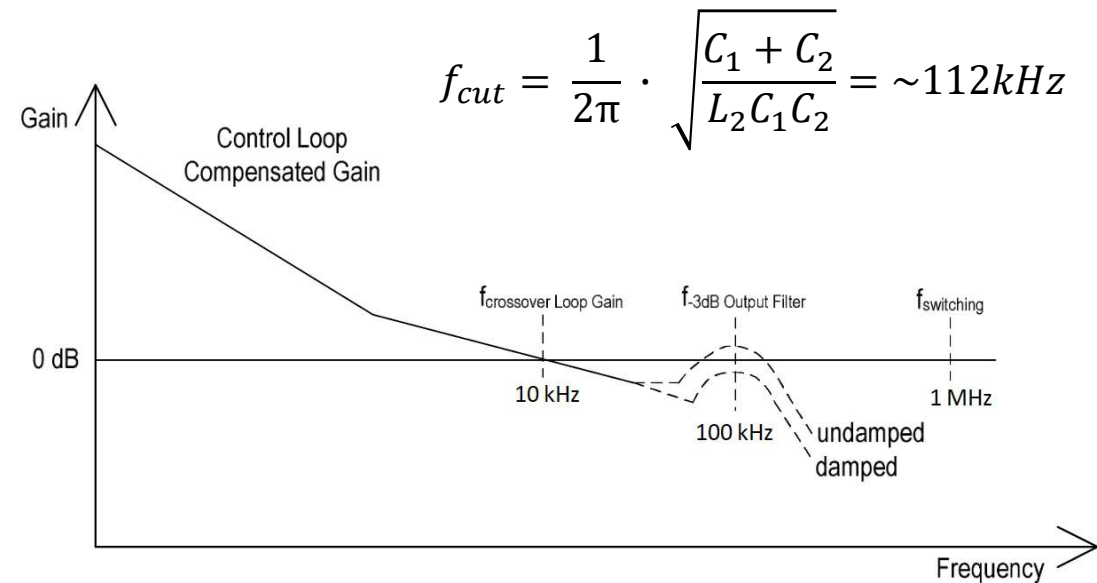
$$V_{ripple} = 7.51\text{mV}$$

LC FILTER CALCULATIONS

Design rules

- Second coil L_2 should be much smaller than L_1 :
 - $L_2 = \frac{1}{10} \cdot L_1 = \frac{1}{10} \cdot 550nH = 55nH$
- Increase of the output capacitor $\rightarrow 541\mu F$
- Second capacitor should be much smaller than the first
 - If not: stability problems can occur
 - $C_1 \rightarrow 47\mu F$

Stability test



TEMPERATURE INTERLOCK SYSTEM

